FIG.1 JUL 1 2 2006 100 PRIMARY SITE 110 120 STORAGE SUBSYSTEM HOST 114 121 CPU 126 111 CPU 122 **MEMORY** 123 MEMORY 112 130 CHA HBA 113~ 127 **RCA** 140 -150 124 CHANNEL EXTENDER 125 160 180 170 SYNC SITE **ASYNC SITE** 

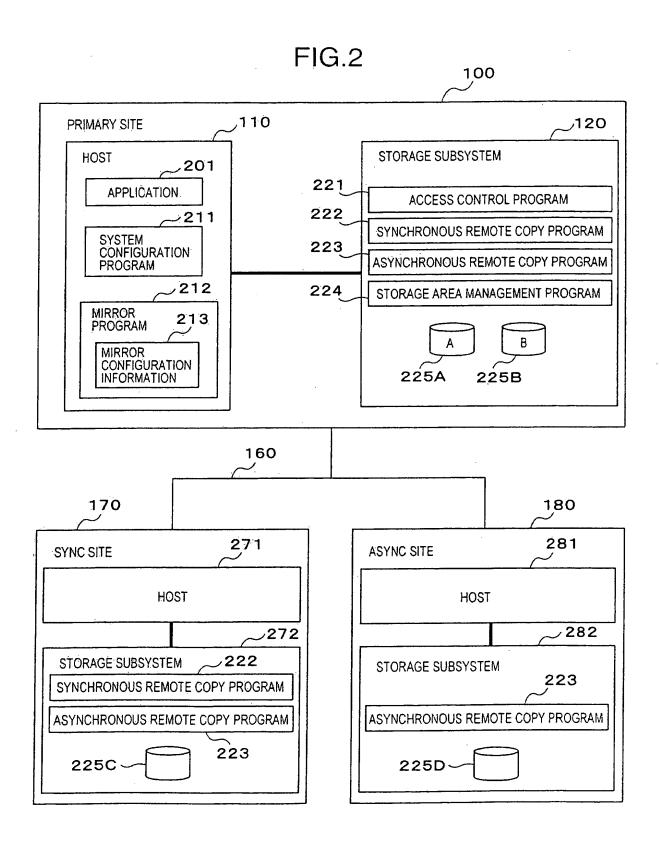


FIG.3

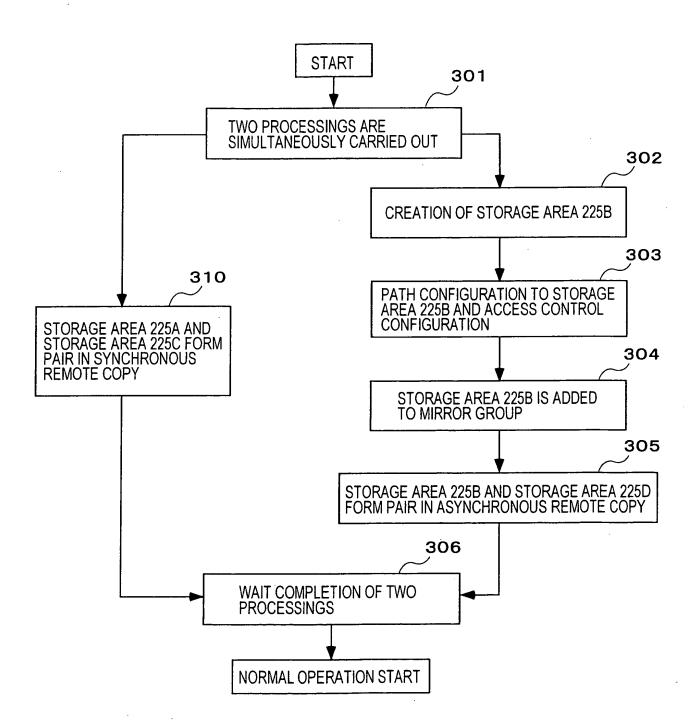


FIG.4

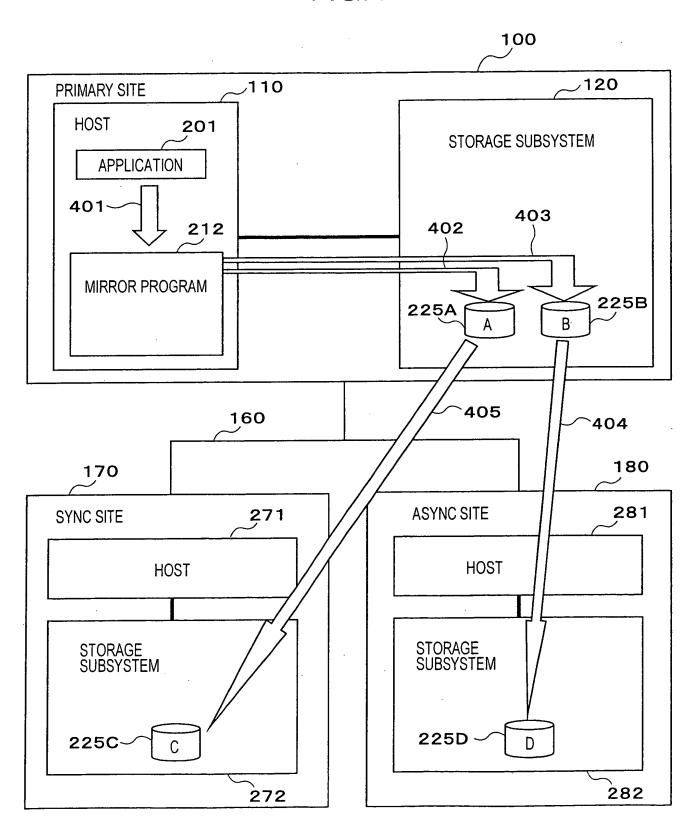


FIG.5

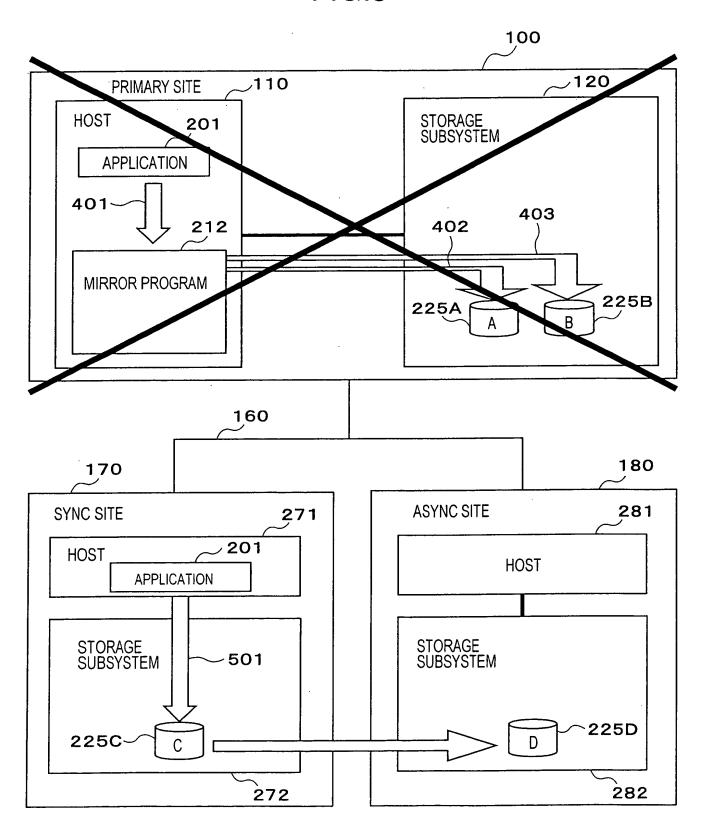


FIG.6

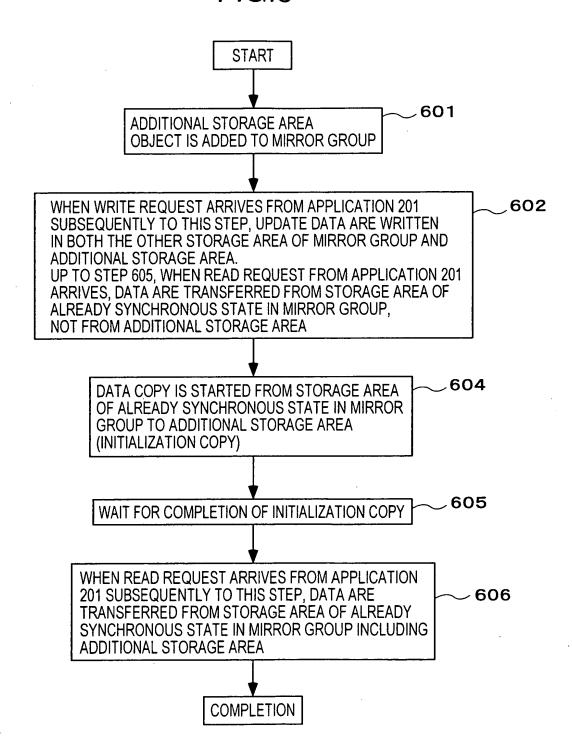


FIG.7

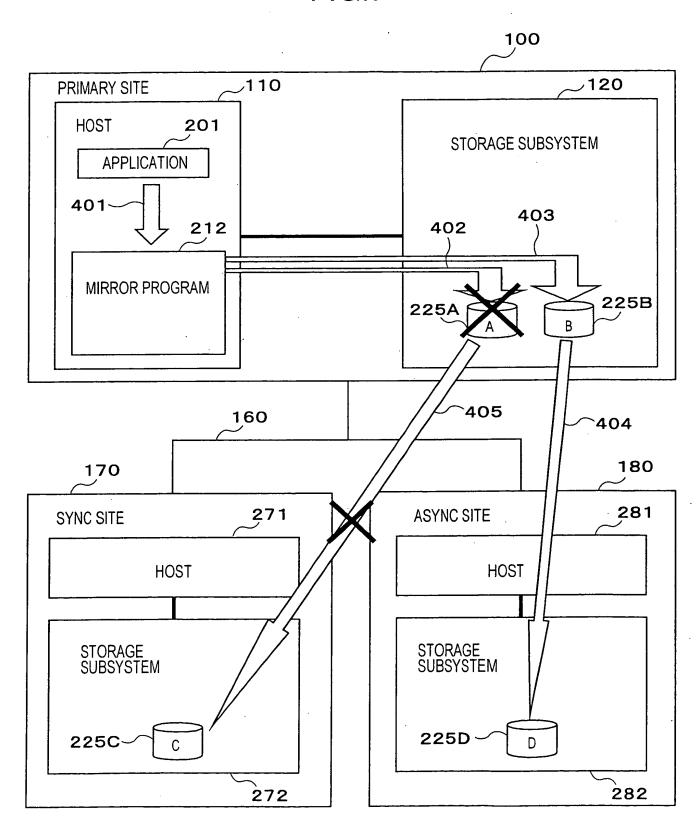


FIG.8

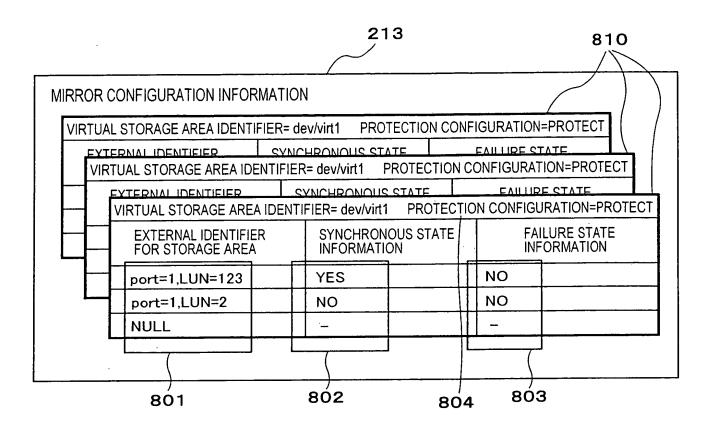
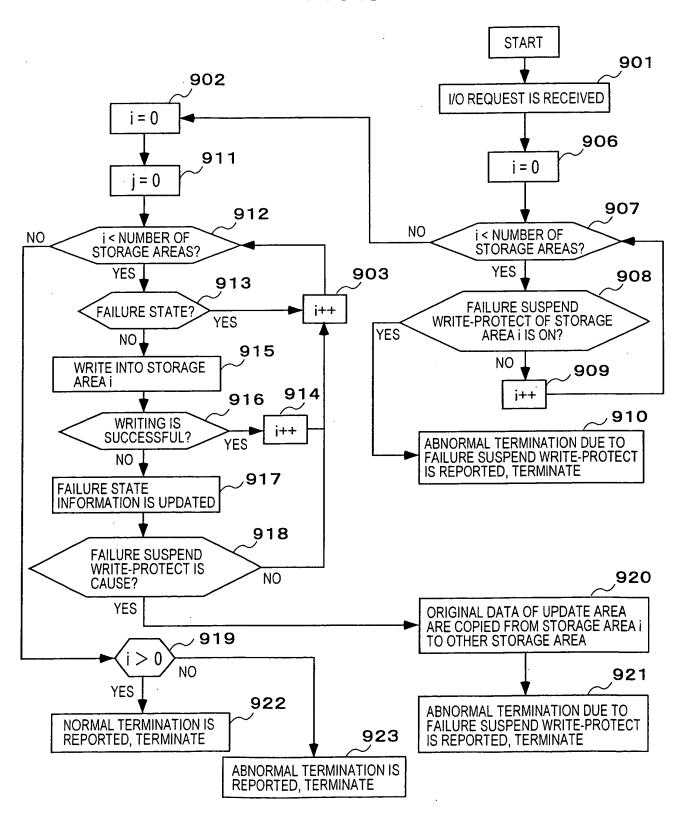


FIG.9



**FIG.10** 

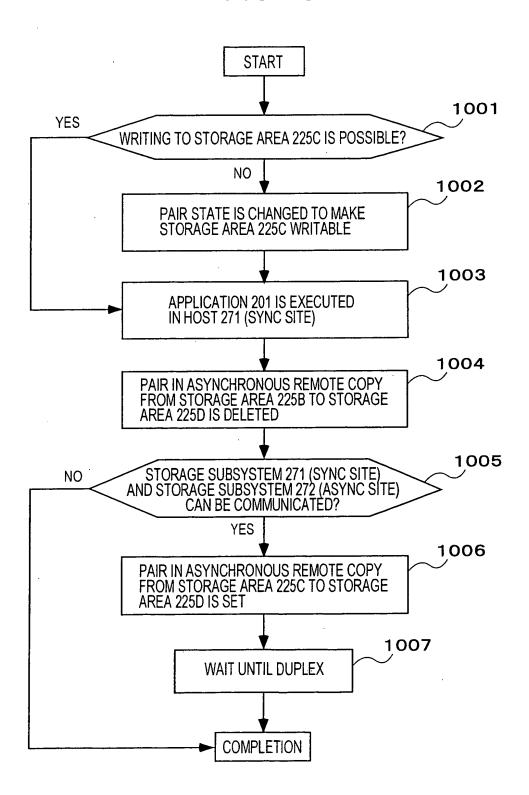
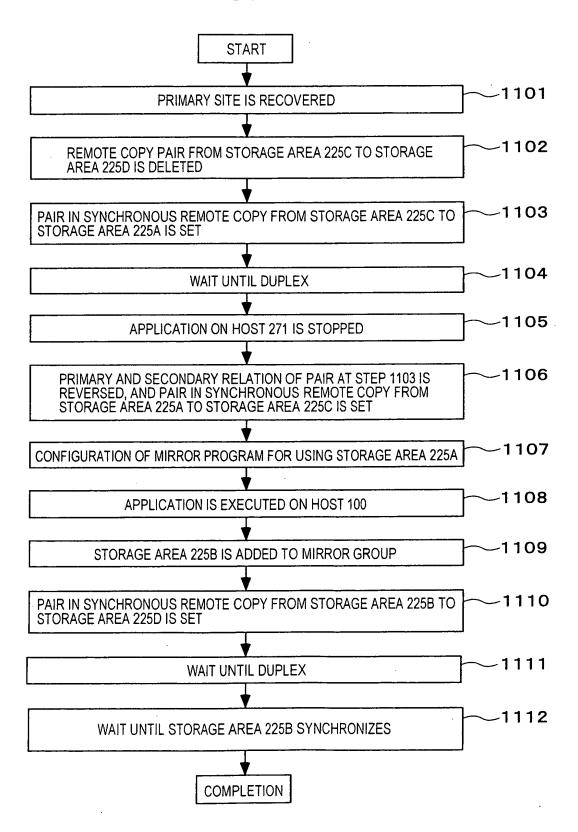


FIG.11



**FIG.12** 

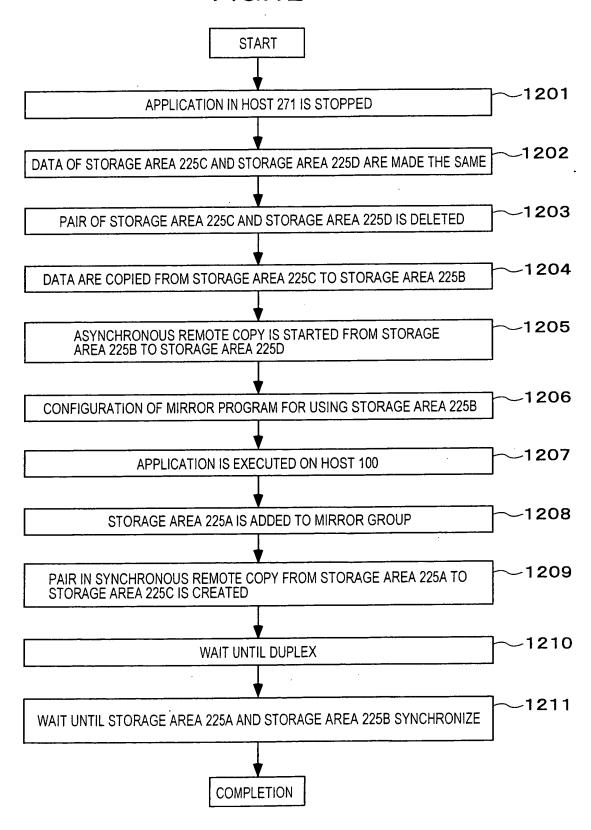
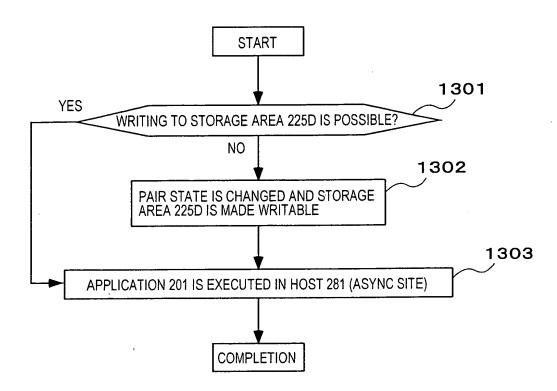
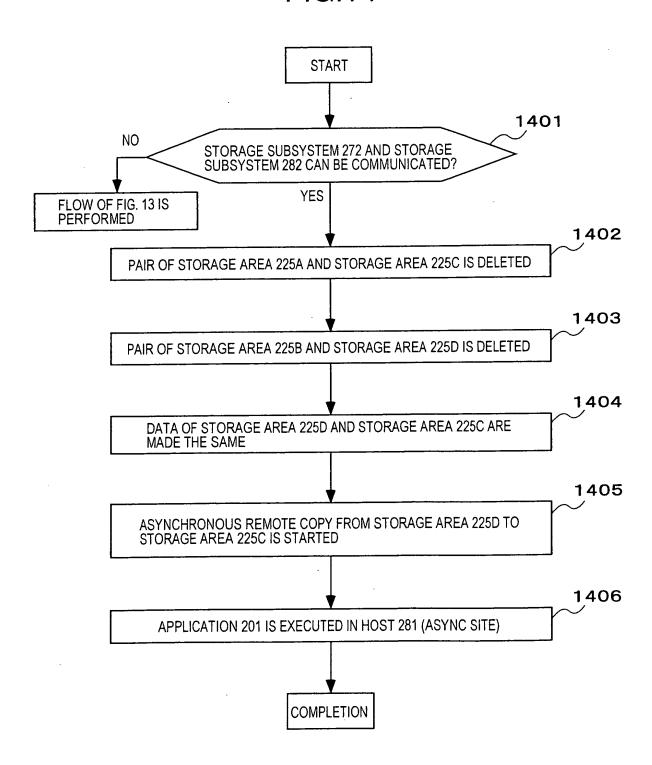


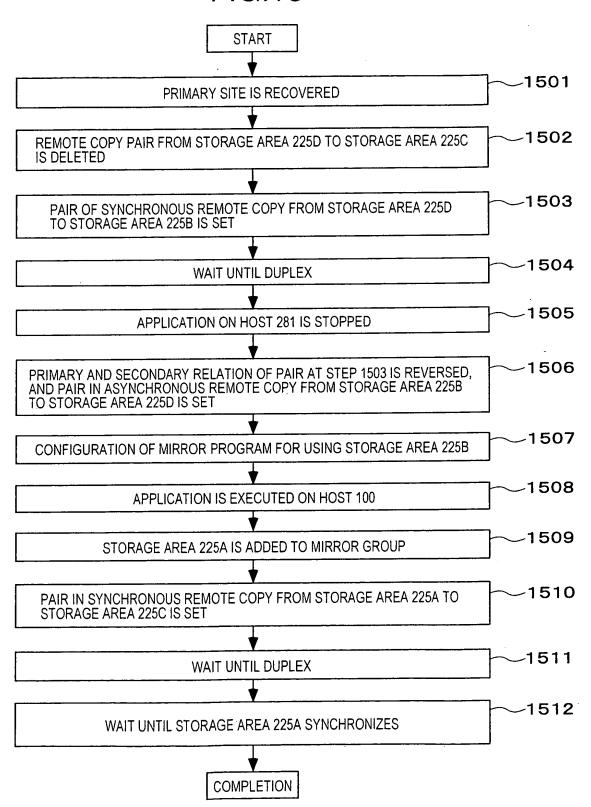
FIG.13



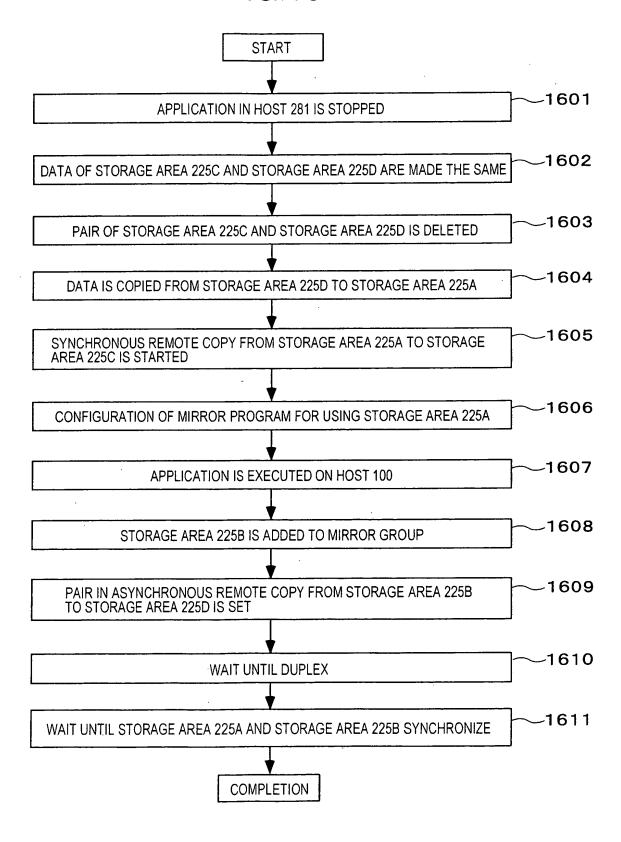
**FIG.14** 



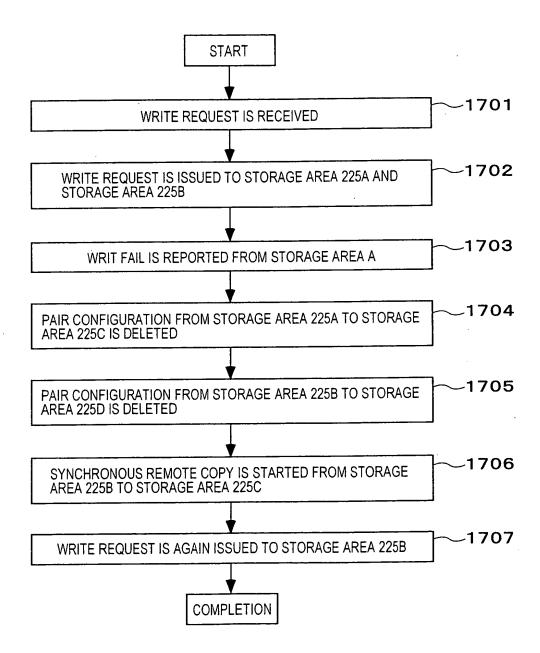
**FIG.15** 



**FIG.16** 



**FIG.17** 



**FIG.18** 

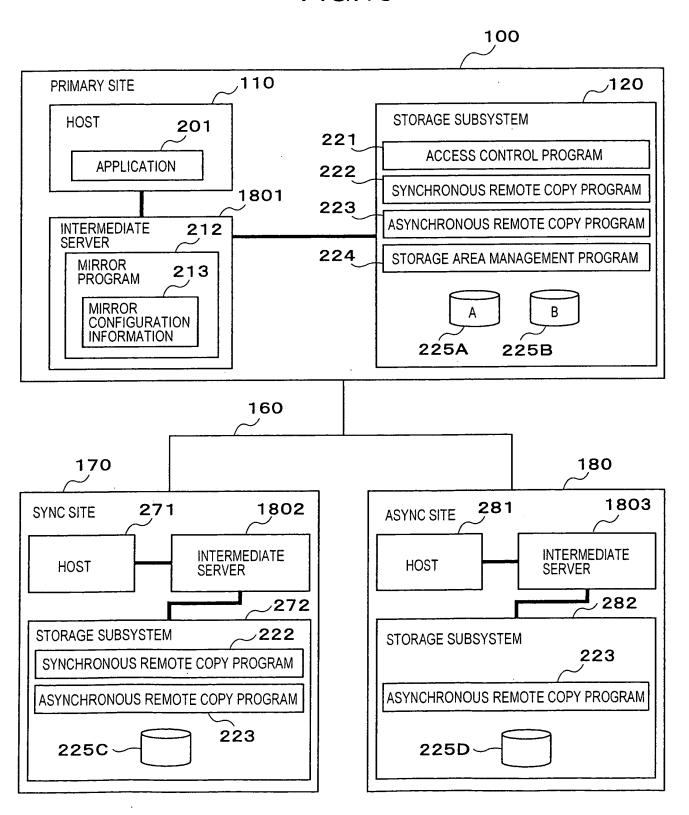
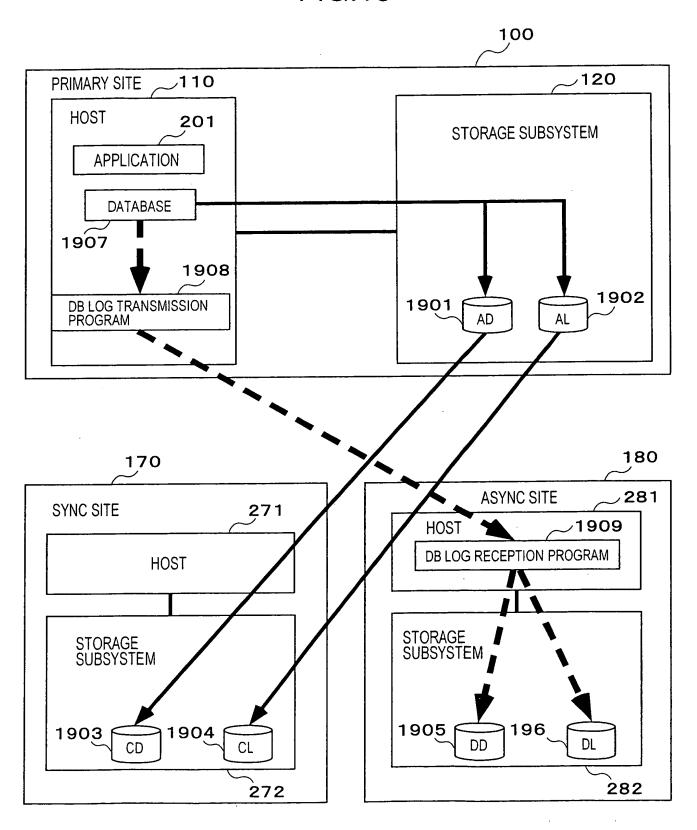


FIG.19



**FIG.20** 

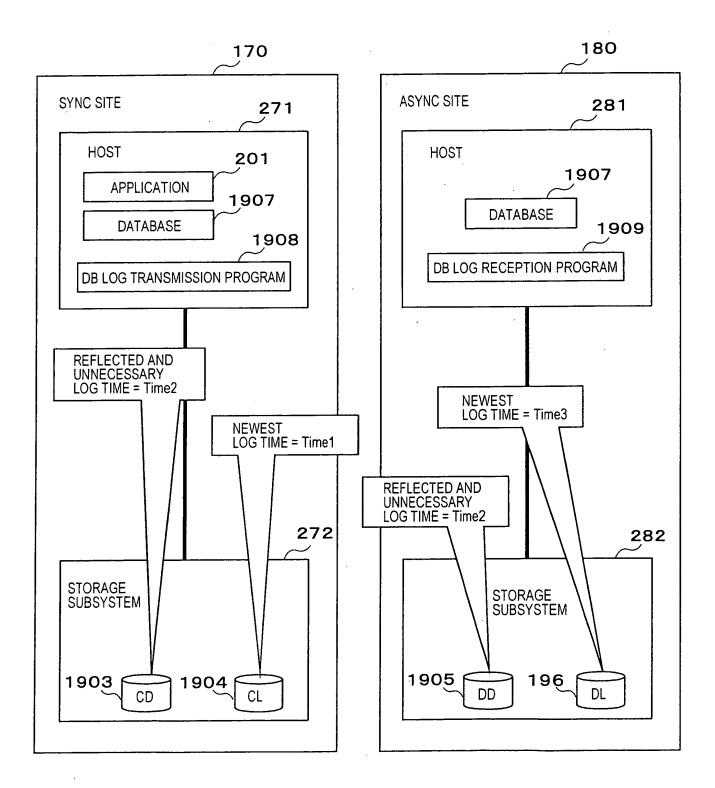


FIG.21

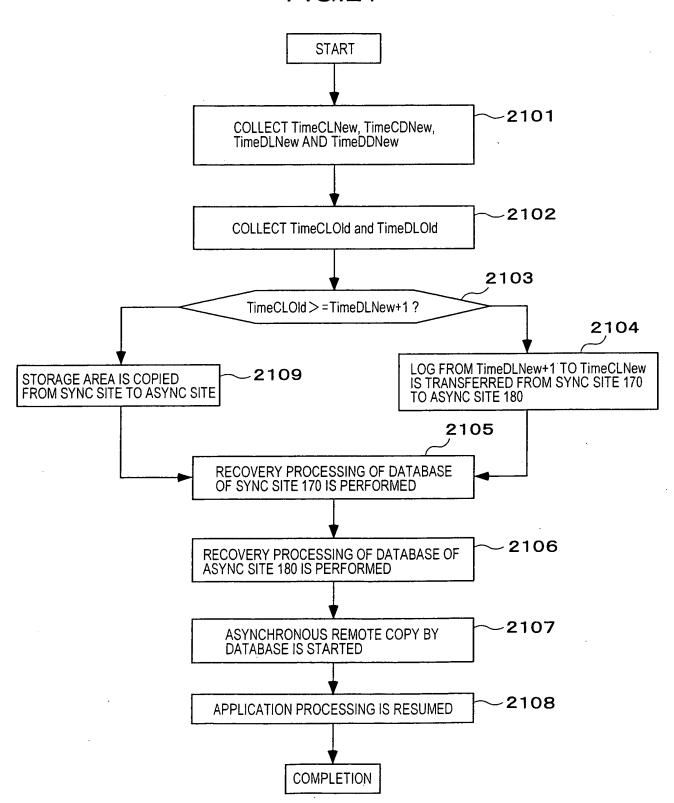


FIG.22

